Abstract

The present invention comprises a userprogrammable control circuit for use in a power converter to automatically transition the converter 5 into BURST mode when load current demand is low. control circuit senses load current demand by monitoring the output current of the converter, and generating a signal representative of the monitored output current. The control circuit may automatically 10 transition the converter into BURST mode when the signal indicative of the average monitored output current decreases below a user-programmable threshold. BURST mode may increase overall converter efficiency by turning OFF a plurality of electronic components, and 15 maintaining the converter's output voltage at a regulated level by energy stored in an output capacitor.